

# SDR-Based Experiments for LTE-LAA Based Coexistence Systems with Improved Design

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**Abstract**— To enhance spectrum efficiency in next generation heterogeneous wireless systems, it is important to improve shared spectrum usage between unlicensed long-term evolution (LTE) systems, such as the license-assisted access (LAA), and legacy systems, such as wireless local area networks (WLANs). LTE-LAA uses listen-before-talk (LBT) schemes to enhance coexistence performance. However, available LAA-LBT schemes may incur several problems, such as a slot-jamming effect and a significant slot boundary tracking error, leading to degraded performance. In this paper, we develop an LBT scheme with improved design and software-defined radio (SDR)-based experimental procedure for a performance validation. We program the improved LBT algorithm in the SDR field-programmable gate array (FPGA), and compare the results with the original LBT algorithm. This work also presents an automated testing technique and new SDR functions that modify the LAA parameters in realtime (such as backoff idle slot durations). The experiment results confirm the predicted slot-jamming effect, and show that our improved design enhances LAA throughput. These results provide not only a more robust LAA-LBT design, but also a new method of SDR programming and testing, and insight into the coexistence performance of heterogeneous systems.

Index Terms: Coexistence; FPGA programming; LTE-LAA; SDR experiments; Test automation; WLAN.

## I. INTRODUCTION

Experimental design and evaluation are critical for performance validation of spectrum sharing techniques between long-term evolution license-assisted access (LTE-LAA) [1]–[6], [11] systems and incumbent systems, such as IEEE 802.11 wireless local area networks (WLANs). To support LTE system development and coexistence, software-defined radio (SDR) or commercial off-the-shelf (COTS)-based experimental methods have become popular, and various SDR test platforms are reported in [3], [7]–[9], [12].

The 3rd Generation Partnership Project (3GPP) LAA has defined four categories of listen-before-talk (LBT) schemes [1], [2] to improve coexistence with legacy systems. In LAA-based coexistence scenarios, LAA nodes may have a larger backoff slot duration than the WLAN counterpart. In [1], [2], LAA uses an extended clear channel assessment (eCCA) slot in the transmission backoff process. The eCCA duration is flexible, and may be  $9\ \mu\text{s}$  to  $20\ \mu\text{s}$ , or larger. The

WLAN backoff slot duration is  $9\ \mu\text{s}$  (which includes CCA time) for several popular physical layer specifications [13]. Various coexistence settings based on LTE-LAA and WLAN transmissions have been evaluated, and laboratory and field test results are reported in [1]–[3], [12]. In particular, SDR devices were used in LAA and WLAN coexistence testing in [12]. However, none of these experimental results report the effect of heterogeneous LAA and WLAN idle slot durations on coexistence performance. In [17], we study the effect of heterogeneous idle slot duration and point out a slot-jamming (SJ) effect of WLAN nodes against LAA nodes in the transmissions backoff process. An anti-SJ-LBT scheme is proposed and its performance is analyzed and verified through computer simulation. Yet, experimental validation of the slot-jamming effect and its mitigation was still absent.

In an LAA transmission cycle, the time slots are divided into idle (backoff), transmission, channel-busy slots, etc. The slot-tracking in different transmission links need to align to maintain a satisfactory performance. In practice, synchronism (or near-synchronism) of slot boundaries has been assumed in the majority of system design, analysis and optimization results [1], [2], [14]–[20], though very often not explicitly discussed. However, the tracking of slot boundaries, such as in the instant when the channel switches from busy to idle, is a nontrivial issue. The LBT process defined in [1], [2] uses an initial CCA (iCCA) duration or a deferred extended CCA (DeCCA) duration to track this channel state change. The iCCA and DeCCA durations are typically suggested to be equal to the WLAN distributed coordination function interframe space (DIFS) duration, which is  $34\ \mu\text{s}$  for the IEEE 802.11a, 802.11n and 802.11ac specifications in the 5 GHz industrial, scientific, and medical (ISM) band [13]. To search for the instant that the channel turns from busy to idle, the LBT uses DeCCA duration as search step size for channel status tracking. However, since the DeCCA duration is much larger than the WLAN backoff idle slot duration, the original LBT design may lead to a significant slot-tracking error. Unsatisfactory slot-tracking accuracy can cause increased transmission collisions and reduced throughput. To our knowledge, the effect of this type of slot boundary tracking error has not been investigated in the literature of LAA-based coexistence systems. Experiment-based study and countermeasure design are important to address this problem.

In this paper, we address the SJ and the slot-tracking error problems in the original LBT process, develop an improved LBT scheme with anti-SJ and subslot-tracking features, and

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implement SDR-based experimental validation. In [3], [7]–[9], [12], the capability of changing LAA LBT parameters in realtime for automated testing was neither discussed nor demonstrated. We note that there is a significant technical gap that needs to be bridged to allow flexible system parameter updates in realtime.

For this purpose, we program the improved LAA LBT function and WLAN functions, based on major modifications of available SDR software related to [12]. We implement SDR field-programmable gate array (FPGA) programming to allow flexible LAA LBT functions, where iCCA and eCCA slot durations can be modified in realtime. We also add user datagram protocol (UDP)-based communication functions to switch parameters and data between the host code and SDR-control codes. Then, we implement conducted tests assuming an additive white Gaussian noise (AWGN) channel in the LAA link, the WLAN link, and the LAA-WLAN mutual interference link.

The contributions and novelty of this paper are summarized as follows:

- We develop an improved LBT method to reduce the slot-jamming effect and slot-tracking error. This scheme is compatible with the existing 3GPP LAA LBT framework.
- We develop an FPGA programming method which implements our new LBT algorithm, and allows the updating of many LAA LBT parameters in realtime. This also includes a test-automation method between computer and SDR devices.
- Experimental results confirm the SJ effect, verify our analysis of the anti-SJ-LBT scheme in [17], and demonstrate enhanced throughput performance of the new LBT method relative to the original LBT.

These results address critical problems of LBT SJ effect and slot boundary alignment (tracking) error in system coexistence, and provide a countermeasure scheme. They also provide new SDR programming and demonstrate the capability to implement automated testing of coexisting systems.

## II. SYSTEM MODEL AND IMPROVED DESIGN

Our testing scenario supposes that LTE-LAA nodes utilize unlicensed spectrum in the downlink and share it with incumbent WLAN nodes. The LTE-LAA system uses LBT to track channel status and makes backoff or transmission decisions. Refer to Fig. 1. After the transmission opportunity (TXOP) of an active link (say, a WLAN link) is completed, the transmission node starts the DIFS, followed by random backoff slots with slot duration of  $9 \mu s$ . Here, for the sake of brevity we do not show the short interframe space (SIFS) and acknowledgement (ACK) signal durations. Note that the active link may also be an LAA link.

Suppose that there are three LAA nodes listening to the channel. To facilitate smooth coexistence, we assume that  $T_{DIFS} = T_{Defer}$ , where  $T_{DIFS}$  and  $T_{Defer}$  are WLAN DIFS and LAA deferred eCCA durations, respectively. Ideally, after the transmission of the WLAN link is completed, all the listening nodes would immediately start the DeCCA period.

The first link has perfect slot alignment with the WLAN link, and starts the eCCA slot synchronously with the WLAN

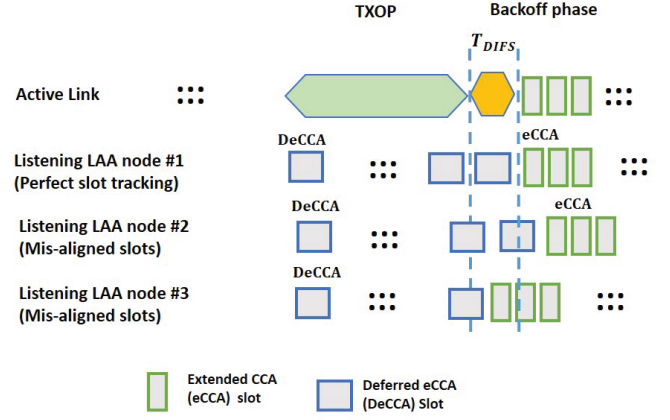


Fig. 1: The iCCA (or DeCCA) slot-tracking errors in the original LAA-LBT process.

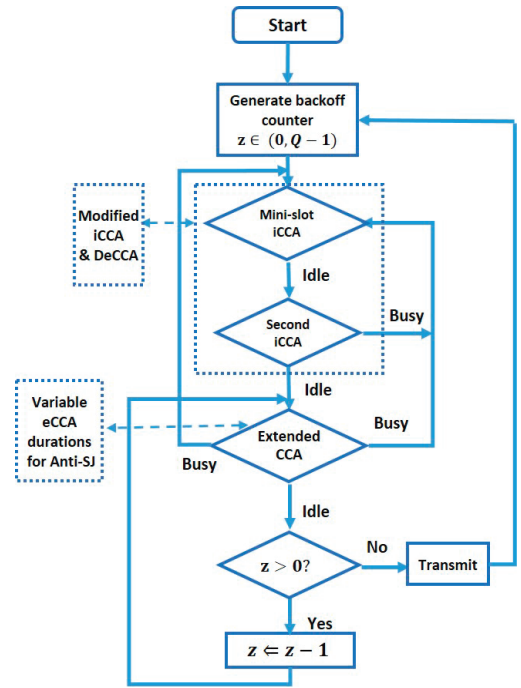


Fig. 2: An improved LAA-LBT scheme that reduces slot-tracking errors and the slot-jamming effect.

link. However, the second and third LAA nodes have major slot-tracking errors with respect to the WLAN link, so that node #2 starts the backoff count-down too late, and node #3 starts it too early.

We explain this phenomenon next. Based on the LBT process in [1], [2], during the channel busy time, the LAA node freezes its backoff process for a DeCCA period (which could be  $34 \mu s$  as a default value). The energy detection (ED) output of each DeCCA duration at each LAA node is compared with a threshold to declare the channel busy or idle. After the channel status changes from busy to idle, the TXOP stop instant can lie in any range in a DeCCA window with duration  $34 \mu s$ . This

inherently causes a slot boundary alignment error in the range  $(-17, 17) \mu s$ , or larger. Another weakness of this design is the sensitivity to the ED threshold setting. A low ED threshold may cause the DeCCA slot to be declared as channel-busy, and the LAA node will continue with one additional DeCCA slot, as shown for LAA node #2. A high ED threshold may cause the DeCCA slot to be declared as channel-idle, and the LAA node will start the eCCA slot too early, as shown for LAA node #3. In a WLAN network, WLAN nodes may use a network allocation vector (NAV) in the packet header to determine the precise time that a transmission stops. However, we are not aware of an LAA specification that provides a method similar to the NAV for the LAA nodes to track the precise stop time of WLAN transmissions. Thus, the slot boundary tracking error becomes a significant issue in LAA and WLAN coexistence cases.

To address this problem, we propose a modified LBT process with subslot-tracking shown in Fig. 2. We merge the iCCA and DeCCA slots of the LBT for convenience, similar to that in [2]. As proposed in [16], we switched the order of blocks “Extended CCA” and “ $z > 0$ ” to remove the channel access priority of an LAA node, which just finished a successful transmission. Compared to available LBT schemes, such as those in [1], [2], [16], in Fig. 2 we split the iCCA/DeCCA slot into two sections, namely, “mini-slot iCCA” and “second iCCA” (where the word “DeCCA” is suppressed for convenience of notation). The “mini-slot iCCA” senses the channel with a much shorter duration than a regular iCCA/DeCCA slot, searching for the precise instant when a TXOP is over. Its purpose is to achieve precise slot boundary alignment. When the sensed signal power (or energy) exceeds the ED threshold (channel busy), the mini-slot iCCA is repeated until the channel is declared idle. When the ED result indicates an idle channel, the LBT process moves to the second iCCA sub-slot and senses for a larger time window. The purpose of this part is to be compatible with WLAN DIFS duration. Note that the sum duration of the “mini-slot iCCA” and “second iCCA” in Fig. 2 can be set equal to  $T_{DIFS}$  ( $34 \mu s$ ), or any iCCA/DeCCA duration specified by LAA network designers.

This improved design may significantly reduce slot-tracking errors. Assuming that channel sensing in the “mini-slot iCCA” is reliable, the slot-tracking error is now bounded by the duration of the mini-slot iCCA (for example,  $4 \mu s$ ), instead of the regular DeCCA duration. This design is compatible with available 3GPP LBT design procedures, because it only involves a change of internal functions in the iCCA and DeCCA blocks. We programmed this method in FPGA code and loaded it onto SDR devices for verification.

Additionally, we experimentally study the impact of LBT SJ effect and the anti-SJ design. We define  $\delta_L$  and  $\delta_W$  as the backoff idle slot durations for LAA and WLAN systems, respectively. In [1], [2], the LBT backoff idle slot is called the LBT eCCA slot, with a flexible duration  $\delta_L$  that can range from  $9 \mu s$  to  $20 \mu s$ , or larger. For several popular physical layer specifications [13] in the 5 GHz ISM band, the WLAN backoff slot duration is set to  $\delta_W = 9 \mu s$ . Our recent work [17] shows that as the ratio  $N_s = \delta_L/\delta_W$  increases, there is

an increasing chance the WLAN nodes jam the transmission opportunity of LAA nodes. We call this effect “slot jamming”, where the LAA throughput decreases and WLAN throughput increases as  $N_s$  increases. We propose an anti-SJ-LBT in [17], which uses a variable eCCA duration based on channel status to enhance LAA transmission opportunity. This feature is shown in Fig. 2. Note that the original LBT uses a fixed eCCA duration, and we call it SJ-LBT in this paper.

### III. EXPERIMENTAL SETUP

Figure 3 shows a block diagram of the conducted measurement setup. A photo of part of this setup is shown in Fig. 4. The goal of this setup is to experimentally examine the slot-jamming and slot-tracking error effects, and the performance of the improved LBT design.

The software structure is shown in Fig. 5, which consists of host programming and SDR FPGA target programming. The host code runs on the computer, and includes a control interface. The control interface code that we programmed sends parameters to commercial LAA and WLAN codes via one set of UDP ports, reads test results via another set of UDP ports, and saves the results to data files of pre-specified formats on the computer hard drive for record and analysis. On the host computer two software projects run simultaneously and control the two SDR units, respectively. Two SDR devices emulate an LTE-LAA link and an WLAN link, respectively, or two LAA links. The communication channels were implemented with power combiner and splitter, and are AWGN channels, as shown in Fig. 3.

The first SDR device implements some LTE-LAA functions. The RF loopback mode was used: the eNode B (eNB, or base station) transmitted data to the user equipment (UE) via the RF ports and cable connections (downlink only), and did not receive any data from the UE via the RF channel (uplink). Some necessary handshaking signals in the uplink and synchronization functions are implemented internally on the FPGA, because the FPGA on a single SDR device emulates both eNB and UE functions.

The LTE-LAA code used on the SDR was a commercial implementation that we modified for use in these experiments. Several significant modifications to this commercial software included: implementation of new LBT functions, ability to adjust the LBT parameters in realtime, addition of a customized channel reservation signal (CRS) to mitigate LAA signal generation and transmission delay, and the addition of an automated testing ability.

The commercial FPGA code we used included an original LBT function but it was modified to add the improved LBT scheme with subslot-tracking and anti-SJ functions. The original DeCCA (or iCCA) state is split into two sub-states with duration of  $4 \mu s$  and  $30 \mu s$ , respectively. The state transitions among many LBT states are updated in FPGA, such as eCCA, mini-slot iCCA, iCCA, DeCCA, counter reduction, and transmission. Also, to implement the anti-SJ-LBT, the eCCA duration on FPGA is updated in realtime based on channel sensing results.

The ability to change the LBT parameters in realtime was achieved by adding several new registers (related to eCCA

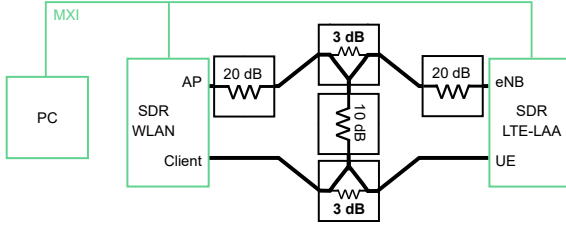


Fig. 3: Conducted SDR test setup for the LAA and WLAN system experiment (with two links).



Fig. 4: Photo of part of the conducted SDR test setup.

and iCCA/DeCCA slot durations) to the FPGA code. These registers were also added to the FPGA-host interface.

The SDR software and devices we used had a significant LAA signal generation and transmission delay of about  $50\sim 90\ \mu\text{s}$ . This delay is defined as the duration starting from the instant that the LAA transmission decision is made until the instant that the LAA signal accesses the channel. Unfortunately, this delay can cause significant conflicts in channel access of LAA and WLAN nodes, and lead to increased collisions and degraded throughput. As a mitigation scheme, we added a new CRS generation function before the LAA signal transmission in FPGA code. This customized CRS is generated as soon as the LAA channel access decision is made, and transmitted with a negligible delay. This CRS has an adjustable duration in an host user-interface, and is seamlessly followed by the actual LAA signal transmission. This method basically solved the LAA transmission delay problem.

Finally, an automated testing interface was added to a host user-interface code that we wrote. This host code communicates via UDP with the commercial LAA and WLAN SDR codes that we modified with new FPGA functions. The modified LAA and WLAN codes accept and recognize automated commands and parameters and send back test results in realtime. These commands were not sent through the RF data link and did not cause overhead to the LAA and WLAN transmissions.

The WLAN implementation on the second SDR unit was achieved by use of commercially available software without additional FPGA programming. Additional changes were made to the host portion of the WLAN software, such as UDP-port communication and automated-testing modules. The WLAN communication link also used an RF loopback mode,

similar to the case of LAA link. Only downlink WLAN traffic was simulated.

We modified the host software to allow online updating of several parameters in both LAA and WLAN links. The parameters include: transmit power, carrier frequency, ED threshold, throughput type, eCCA, mini-slot iCCA, and iCCA (or DeCCA) durations. However, during the measurements, only the eCCA duration was changed online.

During testing, the host SDR code sends commands to the SDR to update parameters at 0.5-second intervals, and receives test results from the SDR in one-second intervals. The result for each parameter setting is averaged over a specified number of received samples, such as 50. Then, the parameter of interest is updated. After requesting the next update, the software waits for ten seconds to allow the SDR devices to stabilize. Following the ten-second wait, the reading of the next set of samples begins.

This experimental setup enables the examination of coexistence theory and computer simulation in a highly controlled manner. Both the LTE and WLAN implementations used for conducted measurements are SDR representations designed for development purposes and are not commercially functioning networks.

#### IV. EXPERIMENTAL RESULTS

In this section, we provide experimental results of the coexistence performance (Refer to Table I for values of some of the key parameters). We check the impact of two design features for LBT that may bring performance improvement for LAA links: subslot-tracking and anti-SJ. Note that the original LBT scheme includes neither the anti-SJ nor the subslot-tracking design.

We assume that the WLAN and LAA systems have channels fully overlapped with 20 MHz bandwidth at a center frequency 5.22 GHz in an ISM band. The LAA link uses modulation and coding scheme (MCS) 7 – quaternary phase-shift keying (QPSK) with rate 0.51, and the WLAN link uses QPSK with rate-1/2. When the transmission time efficiency is 100%, the physical layer channel bit rates (CBRs) are  $\text{CBR}_L = 12.216\ \text{Mbit/s}$  for the LAA link, and  $\text{CBR}_W = 12\ \text{Mbit/s}$  for the WLAN link. The LAA TXOP payload duration is given by  $T_{P,L} = 2\ \text{ms}$ . The WLAN payload duration is computed by  $T_{P,W} = 2048 \times 8 / \text{CBR}_W = 1.4\ \text{ms}$ , where 2048 is the payload size in bytes per WLAN TXOP. The payload here may include physical layer headers and frame check sequence. The SDR-measured throughput for an LAA (or WLAN) link refers to correctly-decoded average data rate at receiver.

For the case of only one active LAA link (the WLAN link is not active), the baseline LAA throughput with Category-3 LBT is derived as

$$S_{L,\text{only}} = \frac{T_{p,L} \text{CBR}_L}{T_{p,L} + T_{\text{iCCA}} + \frac{Z_0 - 1}{2} \delta_L}, \quad (1)$$

where  $Z_0$  is the LBT contention window (CW) size, and  $T_{\text{iCCA}}$  is the duration of initial CCA, set to be equal to LBT DeCCA duration and the WLAN DIFS duration ( $34\ \mu\text{s}$ ). Fig. 6 provides a comparison between the theoretical result in (1) and an SDR measurement result for the MCS-7 scheme when

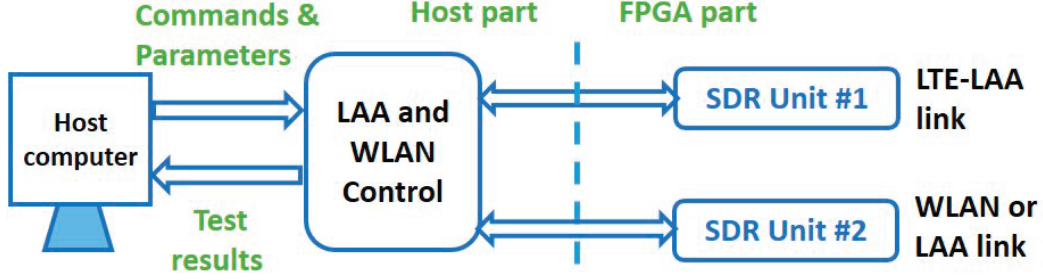


Fig. 5: Software control structure of the LAA and WLAN coexistence performance evaluation in conducted testings.

TABLE I: LTE-LAA and WLAN Parameters in Experimentation.

LAA parameters	
Parameter	Value
Payload duration per transmission	2 ms
Transmit power	15 dBm
CCA ED threshold	-70 dBm
LBT defer period: $T_{\text{Defer}} (=T_{\text{DIFS}})$	34 $\mu\text{s}$
LBT eCCA period: $T_{\text{eCCA}} (=N_s \delta_W)$	$N_s \times 9 \mu\text{s}$
CW size $Z_0$	16
WLAN parameters	
Parameter	Value
Payload duration per transmission	1.4 ms
Transmit power	10 dBm
CCA ED threshold	-72 dBm
$T_{\text{DIFS}}$	34 $\mu\text{s}$
Idle slot duration $\delta_W$	9 $\mu\text{s}$
CW size $W_0$	16

only the LAA link is active. A good match between analytical and measurement results is observed, with relative error of no more than 2%. We have also verified baseline throughput for one active 802.11 WLAN link (while the LAA link is not active) on an SDR device, which approximately matches with the result calculated by use of a method given in [21]. The detail is omitted here for brevity.

Next, we check two cases of LAA-based coexistence: one LAA link with one WLAN link (shown in Figs. 7 and 8), and two LAA links (shown in Fig. 9). In Fig. 7, we show the throughput of LAA (with the original SJ-LBT) and WLAN links. The WLAN idle slot duration is fixed at 9  $\mu\text{s}$ . We observe that as the eCCA duration increases from 9  $\mu\text{s}$  to 45  $\mu\text{s}$ , the LAA throughput decreases while WLAN throughput increases significantly. Also, the LAA throughput is enhanced substantially by using our proposed subslot-tracking method compared to the original LBT which has no feature of subslot-tracking. For example, at  $\delta_L = 9 \mu\text{s}$ , subslot-tracking brings about 0.9 Mbit/s enhancement to the LAA throughput than the case without it.

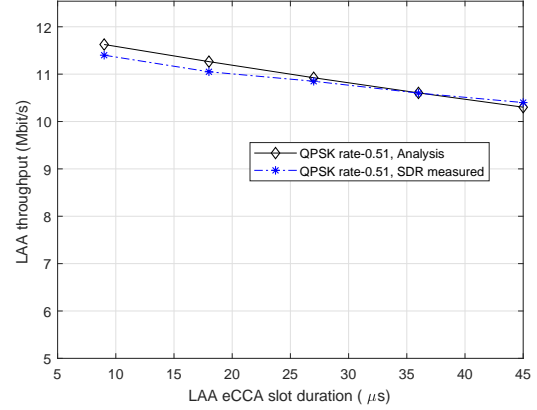


Fig. 6: Analytical and SDR-measured throughput of a single LTE-LAA link vs. eCCA slot duration.

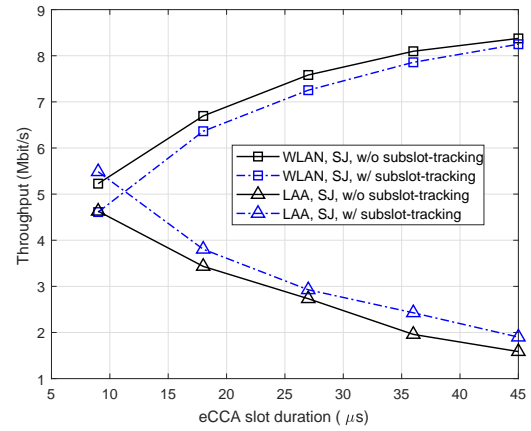


Fig. 7: SDR-measured throughput of the LAA with slot-jamming LBT and WLAN links vs. eCCA slot durations, either with or without subslot-tracking.

In Fig. 8, the throughput of LAA with the anti-SJ-LBT is provided. Comparison between Figs. 8 and 7 demonstrates that the anti-SJ-LBT provides a significantly higher throughput than the original SJ-LBT, either with or without subslot tracking. For example, with subslot-tracking, when  $\delta_L = 45 \mu\text{s}$ , the anti-SJ-LBT provides a throughput of about 3.2 Mbit/s and the



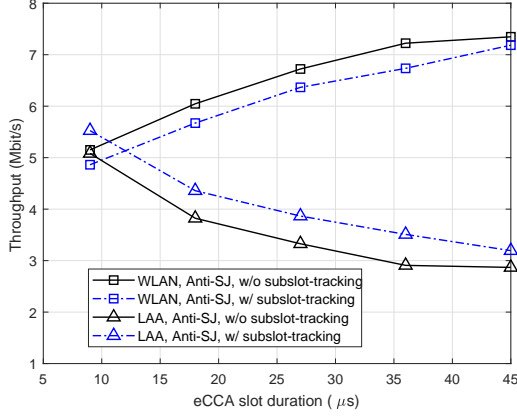


Fig. 8: SDR-measured throughput of the LAA with anti-slot-jamming LBT and WLAN links vs. eCCA slot durations, either with or without subslot-tracking.

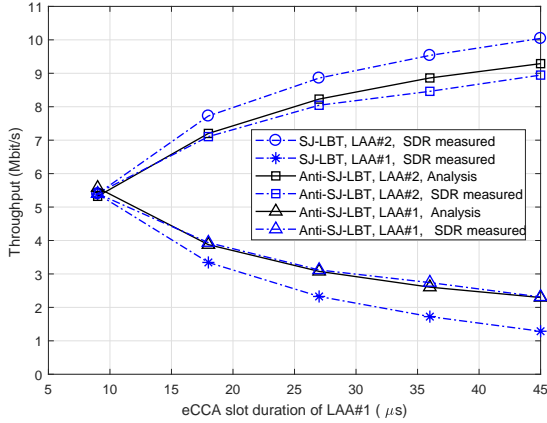


Fig. 9: Analytical and SDR-measured throughput of two LAA links with a fixed and a variable eCCA slot durations, respectively, with subslot-tracking.

original SJ-LBT has a throughput of about 2.0 Mbit/s. Results in Figs. 7 and 8 confirm the effectiveness of subslot-tracking and anti-SJ methods on enhancing the LAA throughput.

Finally, we show throughput of two LAA links in Fig. 9. Link #2 has a fixed eCCA duration (9  $\mu$ s), and link #1 has a variable eCCA duration (from 9  $\mu$ s to 45  $\mu$ s), respectively. For the anti-SJ-LBT, both analytical result [17] and SDR measured result on LAA links are provided. The SJ-LBT scheme has not been analyzed, and only its SDR-measured result is provided. The result in Fig. 9 confirms an approximate match between the analysis given in [17] and SDR measurement result, and demonstrates the effectiveness of the anti-SJ design.

## V. CONCLUSION

In this paper, we have described a slot boundary tracking error problem and a slot-jamming effect in the LTE-LAA LBT scheme, and provided an improved LBT design with subslot-tracking and anti-slot-jamming features. We have programmed the new LBT scheme in an FPGA, and implemented testing on coexisting LAA and WLAN links using SDR devices.

Our developed testing method allows LBT parameters to be changed in realtime on the FPGA, and includes an automated testing procedure, which updates many system parameters in realtime. The results demonstrate the effectiveness of subslot-tracking and anti-SJ design methods, and confirm a reasonably good match between our analysis and experimental results. In future work, the effects of various fading channel models will be studied, and radiated testing will be implemented to further evaluate the coexistence performance of LAA-based systems.

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